

FORM PTO-1390 U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTORNEY'S DOCKET NO. PHD 99-001
TRANSMITTAL LETTER TO THE UNITED STATES DESIGNED/ELECTED OFFICE (DO/EO/US) CONCERNING A FILING UNDER 35 U.S.C. 371		U.S. Application No. (if known, see 37 CFR 1.5) 09/623643
INTERNATIONAL APPLICATION NO. PCT/EP99/10405	INTERNATIONAL FILING DATE December 24, 1999	PRIORITY DATE CLAIMED January 7, 1999
TITLE OF INVENTION MOBILE DATA CARRIER		
APPLICANT(S) FOR DO/EO/US GERRIT W. DEN BESTEN, JOZEF L. W. KESSELS AND VOLKER TIMM		
<p>Applicant(s) herewith submit to the United States Designated/Elected Office (DO/EO/US) the following items and other information:</p> <ol style="list-style-type: none"> <input checked="" type="checkbox"/> This is a FIRST submission of items concerning a filing under 35 U.S.C. 371. <input type="checkbox"/> This is a SECOND or SUBSEQUENT submission of items concerning a filing under 35 U.S.C. 371. <input type="checkbox"/> This express request to begin national examination procedures (35 U.S.C. 371(f)) at any time rather than delay examination until the expiration of the applicable time limit set in 35 U.S.C. 371(b) and PCT Articles 22 and 39(1). <input type="checkbox"/> A proper Demand for International Preliminary Examination was made by the 19th month from the earliest claimed priority date. <input checked="" type="checkbox"/> A copy of the International Application as filed (35 U.S.C. 371 (c)(2)) <ol style="list-style-type: none"> <input checked="" type="checkbox"/> is transmitted herewith (required only if not transmitted by the International Bureau). <input type="checkbox"/> has been transmitted by the International Bureau. <input type="checkbox"/> is not required, as the application was filed in the United States Receiving Office (RO/US). <input checked="" type="checkbox"/> A translation of the International Application into English (35 U.S.C. 371(c)(2)) <input type="checkbox"/> Amendments to the claims of the International Application under PCT Article 19 (35 U.S.C. 371(c)(3)) <ol style="list-style-type: none"> <input type="checkbox"/> are transmitted herewith (required only if not transmitted by the International Bureau). <input type="checkbox"/> have been transmitted by the International Bureau. <input type="checkbox"/> have not been made; however, the time limit for making such amendments has NOT expired. <input type="checkbox"/> have not been made and will not be made. <input type="checkbox"/> A translation of the amendment to the claims under PCT Article 19 (35 U.S.C. 371 (c)(3)). <input checked="" type="checkbox"/> An oath or declaration of the inventor(s) (35 U.S.C. 371(c)(4)). <input type="checkbox"/> A translation of the annexes to the International Preliminary Examination Report under PCT Article 36 (35 U.S.C. 371(c)(5)). <p>Items 11. to 16. below concern document(s) or information included:</p> <ol style="list-style-type: none"> <input type="checkbox"/> An Information Disclosure Statement under 37 C.F.R. 1.97 and 1.98. <input checked="" type="checkbox"/> An assignment document for recording. A separate cover sheet in compliance with 37 C.F.R. 3.28 and 3.31 is included. <input checked="" type="checkbox"/> A FIRST preliminary amendment. <input type="checkbox"/> A SECOND OR SUBSEQUENT preliminary amendment. <input type="checkbox"/> A substitute specification. <input type="checkbox"/> A change of power of attorney and/or address letter. <input checked="" type="checkbox"/> Other items or information: Charge Authorization 4 sheets of Formal Drawings 		

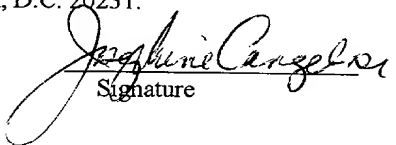
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[X] Express Mail Mailing Label No. *EL 297132121US*Date of Deposit *September 6, 2000*

I hereby certify that this paper and fee is being deposited with the United States Postal Service "Express Mail Post Office to Addresses" services under 37 CFR 1.10 on the date indicated above and is addressed to the Commissioner of Patents and Trademarks, Washington, D.C. 20231.

Josephine Cangelosi

Typed Name



Signature

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533 Rec'd PCT/PTO 06 SEP 2000

U.S. APPLICATION NO. (If known, see 37 C.F.R. 1.5) 09/623643		INTERNATIONAL APPLICATION NO PCT/EP99/10405		ATTORNEY'S DOCKET NUMBER PHD 99-001	
17 [X] The following fees are submitted: BASIC NATIONAL FEE (37 C.F.R. 1.492(A)(1)-(5)):				CALCULATIONS (PTO USE ONLY)	
Search Report has been prepared by the EPO or JPO				\$940.00	
International preliminary-examination fee paid to USPTO (37 C.F.R. 1.482)				\$720.00	
No international preliminary examination fee paid to USPTO (37 C.F.R. 1.482) but international search fee paid to USPTO (37 C.F.R. 1.445(a)(2))				\$760.00	
Neither international preliminary examination fee (37 C.F.R. 1.482) nor international search fee (37 C.F.R. 1.445(a)(2)) paid to USPTO				\$970.00	
International preliminary examination fee paid to USPTO (37 C.F.R. 1.482) and all claims satisfied provisions of PCT Article 33(2)-(4)				\$ 96.00	
ENTER APPROPRIATE BASIC FEE AMOUNT =				\$ 940.00	
Surcharge of \$130.00 for furnishing the oath or declaration later than [] 20 [] 30 months from the earliest claimed priority date (37 C.F.R. 1.492(e)).				\$	
CLAIMS	NUMBER FILED	NUMBER EXTRA	RATE		
Total Claims	4 - 20 =		X \$ 18.00	\$	
Independent claims	1 - 3 =		X \$ 78.00		
MULTIPLE DEPENDENT CLAIMS (if applicable)			+ \$260.00	\$	
TOTAL OF ABOVE CALCULATIONS =				\$ 940.00	
Reductions by 1/2 for filing by small entity, if applicable. Verified Small Entity Statement must also be filed (Note 37 C.F.R. 1.9, 1.27, 1.28)				\$	
SUBTOTAL =				\$	
Processing fee of \$130.00 for furnishing the English translation later than [] 20 [] 30 months from the earliest claimed priority date (37 C.F.R. 1.492(f)).				\$	
TOTAL NATIONAL FEE =				\$	
Fee for recording the enclosed assignment (37 C.F.R. 1.21(h)). The assignment must be accompanied by an appropriate cover sheet (37 C.F.R. 3.28, 3.31). \$40.00 per property				\$ 40.00	
TOTAL FEES ENCLOSED =				\$ 980.00	
				Amount to be Refunded	\$
				Charged	\$
<p>a. [] A check in the amount \$_____ to cover the above fees is enclosed.</p> <p>b. [X] Please charge my Deposit Account No. 14-1270 in the amount of \$ 980.00 to cover the above fees. A duplicate copy of this sheet is enclosed.</p> <p>c. [X] The Commissioner is hereby authorized to charge any additional fee, with the exception of the Base Issue Fee, which may be required, or credit any overpayment to Deposit Account No. 14-1270. A duplicate copy of this sheet is enclosed.</p> <p>NOTE: Where an appropriate time limit under 37 C.F.R. 1.494 or 1.495 has not been met, a petition to revive (37 C.F.R. 1.137(a) or (b)) must be filed and granted to restore the application to pending status.</p> <p>SEND ALL CORRESPONDENCE TO:</p> <p>Corporate Patent Counsel Philips Electronics North America Corporation 580 White Plains Road Tarrytown, NY 10591</p> <p>DATE OF MAILING:</p>					
				(SIGNATURE)	
				Daniel J. Piotrowski (NAME)	
				42,079 (REGISTRATION NUMBER)	

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09/623643

533 Rec'd PCT/PTO 06 SEP 2000

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Atty. Docket

GERRIT W. DEN BESTEN ET AL

PHD 99-001

Serial No.

Filed: Concurrently

MOBILE DATA CARRIER

Commissioner for Patents
Washington, D.C. 20231

PRELIMINARY AMENDMENT

Sir:

Prior to calculation of the filing fee and examination,
please amend the above-identified application as follows:

IN THE CLAIMS

Please amend claims 3 and 4 as follows:

Claim 3, line 1, delete "or 2".

Claim 4, line 1, delete "2 or 3,".

REMARKS

The claims are amended to remove multiple dependency
without change in scope.

Entry is respectfully solicited.

Respectfully submitted,

By 

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Mobile data carrier

The invention relates to a mobile data carrier, particularly a chip card. Such chip cards have also become known as smart cards.

To enable unauthorized persons to investigate data stored on such data carriers, a method has been publicized in which data patterns processed in the data-
5 processing circuit can be inferred from the variation with respect to time of a power supply voltage at the power supply terminals of a data-processing circuit within the data carrier and from the variation with respect to time of a power supply current taken up by the data-
processing circuit at the power supply terminals.

It is an object of the invention to at least considerably complicate the
10 investigation of data patterns by means of the above-mentioned method in a mobile data carrier, or to even render this impossible.

According to the invention, this object is achieved by a mobile data carrier, particularly a chip card, comprising a data-processing circuit and a supply unit for applying electric energy to power supply terminals for operating the data-processing circuit from an
15 external energy source arranged outside the data carrier, the supply unit comprising a voltage-limiting control circuit which is arranged parallel to the power supply terminals of the data-processing circuit, and a current control device which, with respect to the supply of energy to the data-processing circuit, is arranged in series with the parallel arrangement of the voltage-limiting control circuit and the data-processing circuit.

Due to the invention, it is achieved in a simple and advantageous manner that an external energy source arranged outside the data carrier constantly feeds a constant flow of energy into the data carrier. The voltage and the current with which this flow of energy is realized are always constant during the whole operation at circuit points which are accessible from outside the data carrier and thus do not allow any inference from individual data-
20 processing operations within the data-processing circuit. Particularly, it can no longer be concluded from the energy flow in a bit-precise manner whether a special computation has taken place.

For the mobile data carrier according to the invention, particularly contactless or also contacted chip cards, the invention has the function of a power supply source with the

characteristics that, at the receiving end of the path in the arrangement, via which the energy flow for the data-processing circuit is realized, the load is independent of the load on the supplying side of this path, and that the source on the supplying side behaves as a current source via a large control range. Such a power supply source can be constructed by means of
5 a parallel control device. Particularly, the contactless energy supply of a chip card has the characteristic of a constant current source which does not have to be controlled.

An advantageous further embodiment of the mobile data carrier according to the invention is characterized in that the current control device is adapted to supply an at least substantially load-independent supply current, and in that the voltage-limiting control circuit
10 is adapted to take up an excess current which, with respect to the power supply current, is complementary to a power supply current taken up by the data-processing circuit at the power supply terminals.

In accordance with a further advantageous embodiment, the mobile data carrier according to the invention is characterized in that the supply current supplied by the
15 current control device is at least substantially controlled in dependence upon only one voltage supplied by the external energy source.

A further possibility of inference in the investigation method mentioned in the opening paragraph for a mobile data carrier is provided in that, in a data-processing circuit, synchronously clocked logic elements are used in the data carrier, which elements are strictly
20 controllable with respect to time with the aid of the synchronous clock signal. This time control simplifies the assignment of bit patterns to the data-processing operations in the case of an unauthorized access.

In accordance with a preferred further embodiment, the mobile data carrier according to the invention is characterized in that the data-processing circuit is implemented
25 with asynchronously operating logic elements whose signal-processing rate is dependent on a power supply voltage applied to the power supply terminals of the data-processing circuit.

By using asynchronously operating logic elements in the data-processing circuit of the mobile data carrier according to the invention, there may be confusion in the case of unauthorized access. Switching processes which are transferred by such
30 asynchronously operating logic elements to the power supply terminals of the data-processing circuit generate a pattern which cannot be correlated and from which conclusions with respect to processed data patterns cannot be made. Advantageously, the signal-processing rate of the logic elements is dependent on the power supply voltage. The passage of data of the data-processing circuit is thus automatically adapted to the available power

supply voltage. A data-processing circuit formed in this way can function in a wide power supply voltage range while using a comparably small number of components.

In this embodiment, the mobile data carrier according to the invention has the further advantage that there is no interference of signals from the data-processing circuit with signals externally applied to the mobile data carrier or signals supplied to the exterior by the mobile data carrier. The protection against the investigation method mentioned in the opening paragraph is very high because the electromagnetic radiation of the data-processing circuit in mobile data carriers does not even comprise information about data patterns in the data-processing circuit. The power supply current taken up by the mobile data carrier is independent of the processed data patterns. Dependent on the power output of the energy supply to the mobile data carrier, the processing rate in the data-processing circuit is always adjusted to the optimally possible value. As against an implementation of the data-processing circuit with synchronously clocked logic elements, an essentially smaller dimensioned filter capacitor may be particularly used for interference suppression. As regards the investigation of data patterns in the data-processing circuit, an encryption of the time ranges is achieved, in which the data input and output operations and computations are performed. It may be achieved that it is no longer possible to determine from the exterior when an actual computing process or an input or output operation takes place.

These and other aspects of the invention are apparent from and will be elucidated with reference to the embodiments described hereinafter.

In the drawings:

- Fig. 1 is a block diagram of a first embodiment,
- Fig. 2 is a block diagram of a second embodiment,
- Fig. 3 is a detailed representation of a third embodiment,
- Fig. 4 is a detailed representation of a fourth embodiment and
- Fig. 5 is a block diagram of a mobile data carrier according to the invention.

The arrangement shown in Fig. 1 comprises a data-processing circuit 1 with two power supply terminals 2, 3. A power supply voltage is supplied via these terminals to the data-processing circuit 1, which power supply voltage serves for the supply of energy to the data-processing circuit 1. Electric energy is applied via two terminals 4, 5 from an external energy source (not shown) arranged outside the data carrier. The terminals 4, 5 are

connected, for example, to a circuit arrangement which rectifies a transmitted electric voltage in a contactless chip card and makes this rectified voltage available for energy supply to the data-processing circuit 1.

The terminals 4, 5 constitute inputs of a supply unit 6 which comprises a
5 voltage-limiting control circuit 7 and a current control device 8. The voltage-limiting control circuit 7 is arranged parallel to the power supply terminals 2, 3. The current control device 8 is arranged in series with the parallel circuit of the voltage-limiting control circuit 7 and the data-processing circuit 1. This series arrangement is arranged between the terminals 4 and 5. The current control device 8 is adapted to supply an at least substantially load-independent
10 supply current which is denoted in Fig. 1 by means of the arrow with the reference numeral 9. At least in a given control range, the current control device 8 thus has the behavior of an at least substantially ideal current source. The supply current 9 may be controlled by a voltage which is present between the terminals 4 and 5 so as to adapt the energy supply for the data-processing circuit 1 to this voltage between the terminals 4 and 5.

15 The data-processing circuit 1 is preferably formed with asynchronous logic elements whose signal-processing rate is dependent on the available supply current 9 as a maximum power supply current at the power supply terminals 2, 3. Dependent on the operations performed in the data-processing circuit 1, however, the power supply current, denoted by means of arrow 10 in Fig. 1, will be smaller than the supply current 9. An excess
20 current, shown by way of arrow 11 in Fig. 1, and being complementary to the power supply current 10 with respect to the supply current 9 is depleted through the voltage-limiting control circuit 7. The supply current 9 can therefore be dimensioned data-independently. Moreover, a capacitor 12 is arranged parallel to the power supply terminals 2, 3, which capacitor, particularly in contactless chip cards, is used for avoiding interferences between
25 signals in the data-processing circuit 1 and signals which are received externally or transmitted to the exterior by the chip card. In the embodiment of the mobile data carrier according to the invention, this capacitor 12 may be dimensioned to be very small.

The voltage-limiting control circuit 7 not only has the function of taking up the excess current 11 but also the function of keeping the power supply voltage at the power
30 supply terminals 2, 3 within a predetermined voltage range. The capacitor 12 also serves to average, i.e. compensate load peaks in the data-processing circuit 1. Since these may already be strongly reduced by using asynchronous logic elements, only a small capacitor is required for this purpose.

In the construction shown in Fig. 1, a control loop is not necessary for controlling data-dependent power supply currents, which control loop constantly has a finite control rate and thus, as regards construction, cannot realize a complete suppression of such data dependencies in the energy supply. In the mobile data carrier according to the invention, the supply current 9 is completely constant without any data dependence. Due to the voltage-limiting operation of the control circuit 7, it is achieved that the current control device 8 also remains within its control range at fluctuating power supply currents so that the supply current 9 can be maintained constant.

Due to the control by the current control device 8 after the voltage at the terminals 4, 5, the supply current 9 can be adapted to the power supply current/power supply voltage characteristic of the data-processing circuit 1.

Fig. 2 is a simplified block diagram, in which elements corresponding to those in Fig. 1 have the same reference numerals. The voltage-limiting control circuit 7 is represented by a zener diode in this case, and the current control device 8 is represented by an ideal current source. This current source is shown as an uncontrolled, i.e. non-adjustable current source. The power supply voltage for the data-processing circuit is maintained constant by the zener diode 7 in a wide range of the energy supply via the terminals 4, 5, as well as for the signal-processing operations in the data-processing circuit 1.

The embodiment in Fig. 3 is a more detailed representation of the supply unit 6. Circuit elements already described are denoted again by the same reference numerals. The energy supply via the terminals 4, 5 is effected from a voltage source 13 shown diagrammatically. As an equivalent circuit diagram for the data-processing circuit 1, the parallel arrangement of a current source 14 with the series arrangement of a resistor 15 and a voltage source 16 is shown.

The supply unit 6 shown in Fig. 3 comprises a first and a second transistor 17, 18 whose sources are connected to the terminal 4. The gate of the second transistor 18 is connected to the gate of the first transistor 17 and to its drain. The first and second transistors 17, 18 constitute a current mirror.

The drain of a third transistor 19 is connected to the drain of the first transistor 17 and to a gate of a fourth transistor 20. The gate of the third transistor 19 is connected to the terminal 4. A source of the third transistor 19 is connected to the terminal 5.

The source of the fourth transistor 20 is connected to the drain of the second transistor 18 and to the power supply terminal 2. A drain of the fourth transistor 20 is connected to a drain of a sixth transistor 22 and its gate is connected to a gate of a seventh

transistor 23 and to a drain of an eighth transistor 24. The sources of the sixth and seventh transistors 22, 23 are jointly connected to the terminal 5 so that the sixth and seventh transistors 22, 23 also constitute a current mirror. The source of a fifth transistor 21 is connected to the power supply terminal 2 and its drain is connected to the terminal 5, while its gate is connected to the drain of the seventh transistor 23. Moreover, there is a connection between the gate of the fifth transistor 21 and the source of the eighth transistor 24 as well as the drain of a ninth transistor 25. A source of the ninth transistor 25 is also connected to the power supply terminal 2. While a gate of the ninth transistor 25 is directly connected to the power supply terminal 3, the gate of the eighth transistor 24 is connected to this power supply terminal 3 via a reference source 26.

Fig. 4 is a modification of the embodiment in Fig. 3, in which elements already shown in Fig. 3 are denoted by the same reference numerals. As compared with Fig. 3, the circuit arrangement in Fig. 4 includes a tenth transistor 27 whose gate is connected to the gate of the third transistor 19. The source-drain path of the tenth transistor 27 is incorporated in the connection between the third and the first transistor 19 and 17 in such a way that the drains of the tenth and the first transistor 27, 17 are connected together and the source of the tenth transistor 27 is connected to the drain of the third transistor 19 as well as the gate of the fourth transistor 20. A second capacitor 28 is arranged between the gate of the fourth transistor 20 and the terminal 5.

As compared with Fig. 3, the current mirror of the sixth transistor 22 and the seventh transistor 23 is omitted in Fig. 4. Consequently, the drain of the fourth transistor 20 is directly connected to the terminal 5. Also the drain of the eighth transistor 24 is directly connected to the terminal 5.

Fig. 5 is a block diagram of an embodiment of a mobile data carrier according to the invention, which is adapted both for contacted and contactless connections. Elements corresponding to elements from the previously described Figures have the same reference numerals.

For contactless energy supply and data transmission, the data carrier of Fig. 5 comprises a resonant circuit of an inductance 29 and a third capacitor 30. When high-frequency electric energy is applied via the resonant circuit 29, 30, a voltage is generated therein, which is applied to the terminals 4, 5 via a rectifier arrangement 31. A fourth capacitor 32 which is connected to the terminals 4, 5 is used for sieving and smoothing the voltage at the terminals 4, 5. The energy is supplied to the data-processing circuit 1 via the current control device 8 and the voltage-limiting control circuit 7.

The data carrier shown in Fig. 5 further comprises a clock preparation circuit 33 and a data preparation circuit 34 both of which receive data and clock signals applied via the resonant circuit 29, 30 for separating and signal formation via a line 35. The clock preparation circuit 33 and the data preparation circuit 34 are also supplied with energy from the supply unit 6, which is shown diagrammatically by way of the connection 36 in Fig. 5. Clock signals and data signals are applied to the data-processing circuit 1 via a clock line 37 and a data line 38, respectively.

For the supply of data signals by the data-processing circuit 1, these data signals are applied to a modulation device 40 via a further data line 39, which modulation device influences, for example, the resonant circuit 29, 30 via a load modulation.

For a contacted connection of the mobile data carrier as shown in Fig. 5 with external arrangements for data transmission and energy supply, the clock line 37 is connected to a first contact 41, the data line 38 is connected to a second contact 42, the further data line 39 is connected to a third contact 43 and the terminal 5 is connected to a fourth contact 44.

For the purpose of energy supply, a fifth contact 45 is provided which is connected to the terminal 4. In this way, fluctuations of the power supply current at the power supply terminals 2, 3 do not reach the exterior of the mobile data carrier via the fifth contact 45. This would be the case when instead of the fifth contact 45 a sixth contact 46 were provided which is directly connected to the power supply terminal 2. Direct detections of fluctuations of the power supply current for the data-processing circuit 1 would be possible via such a sixth contact 46.

CLAIMS:

1. A mobile data carrier, particularly a chip card, comprising a data-processing circuit and a supply unit for applying electric energy to power supply terminals for operating the data-processing circuit from an external energy source arranged outside the data carrier, the supply unit comprising a voltage-limiting control circuit which is arranged parallel to the power supply terminals of the data-processing circuit, and a current control device which, with respect to the supply of energy to the data-processing circuit, is arranged in series with the parallel arrangement of the voltage-limiting control circuit and the data-processing circuit.

2. A mobile data carrier as claimed in claim 1, characterized in that the current control device is adapted to supply an at least substantially load-independent supply current, and in that the voltage-limiting control circuit is adapted to take up an excess current which, with respect to the power supply current, is complementary to a power supply current taken up by the data-processing circuit at the power supply terminals.

3. A mobile data carrier as claimed in claim 1 or 2, characterized in that the supply current supplied by the current control device is at least substantially controlled in dependence upon only one voltage supplied by the external energy source.

4. A mobile data carrier as claimed in claim 1, 2 or 3, characterized in that the data-processing circuit is implemented with asynchronously operating logic elements whose signal-processing rate is dependent on a power supply voltage applied to the power supply terminals of the data-processing circuit.

ABSTRACT:

A mobile data carrier is described, particularly a chip card, comprising a data-processing circuit and a supply unit for applying electric energy to power supply terminals for operating the data-processing circuit from an external energy source arranged outside the data carrier, the supply unit comprising a voltage-limiting control circuit which is arranged
5 parallel to the power supply terminals of the data-processing circuit, and a current control device which, with respect to the supply of energy to the data-processing circuit, is arranged in series with the parallel arrangement of the voltage-limiting control circuit and the data-processing circuit.

Investigation of data via a measurement of the power supply voltage or the
10 power supply current for the data-processing circuit is thereby prevented.

Fig. 1

1/4

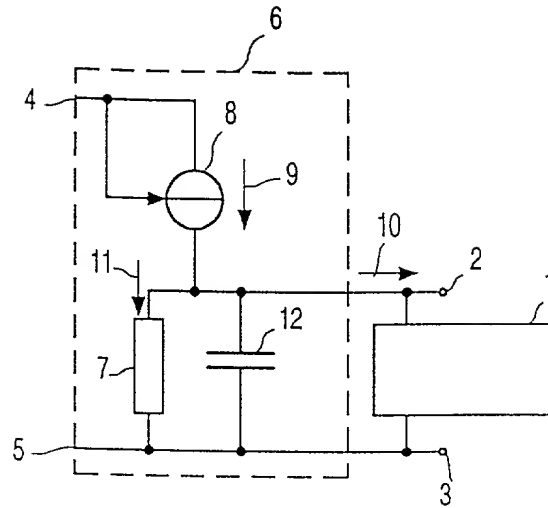


FIG. 1

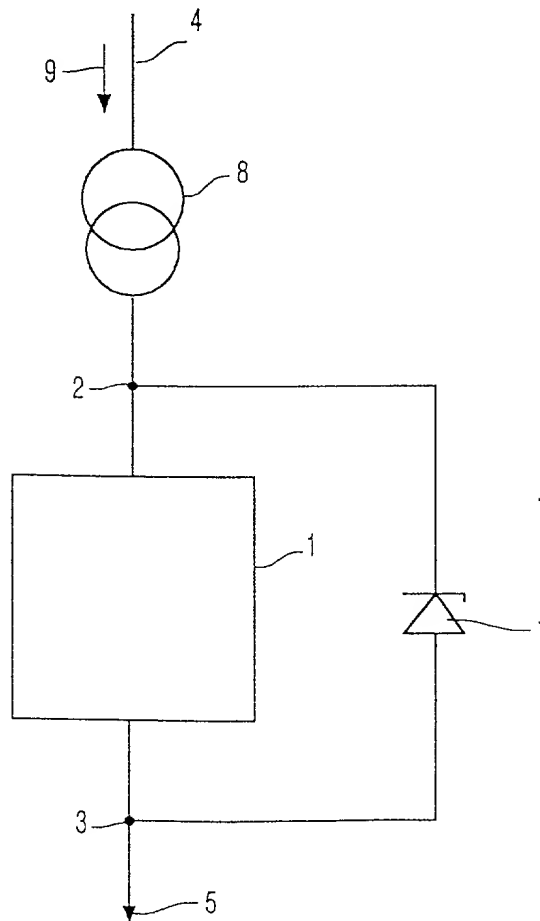


FIG. 2

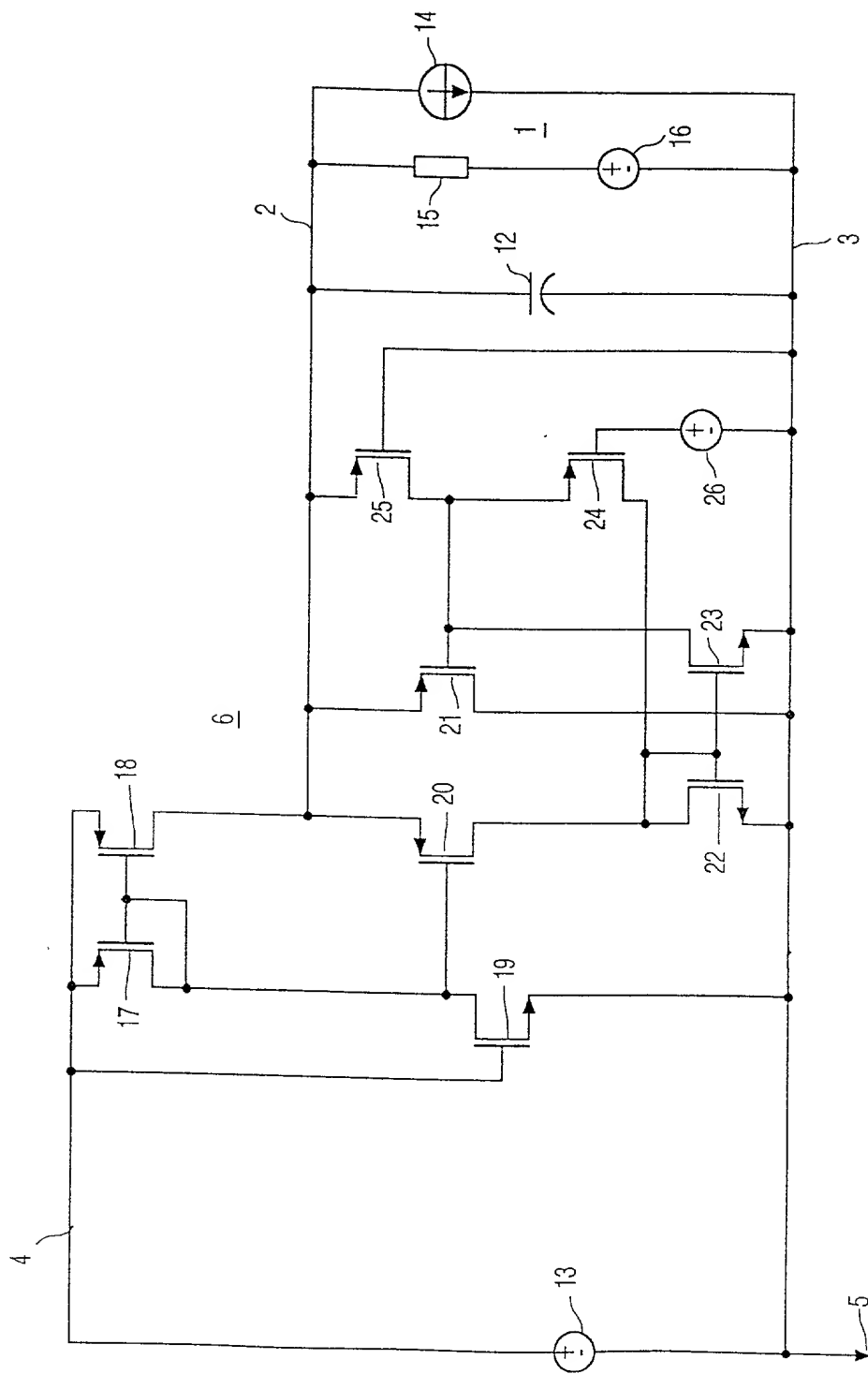


FIG. 3

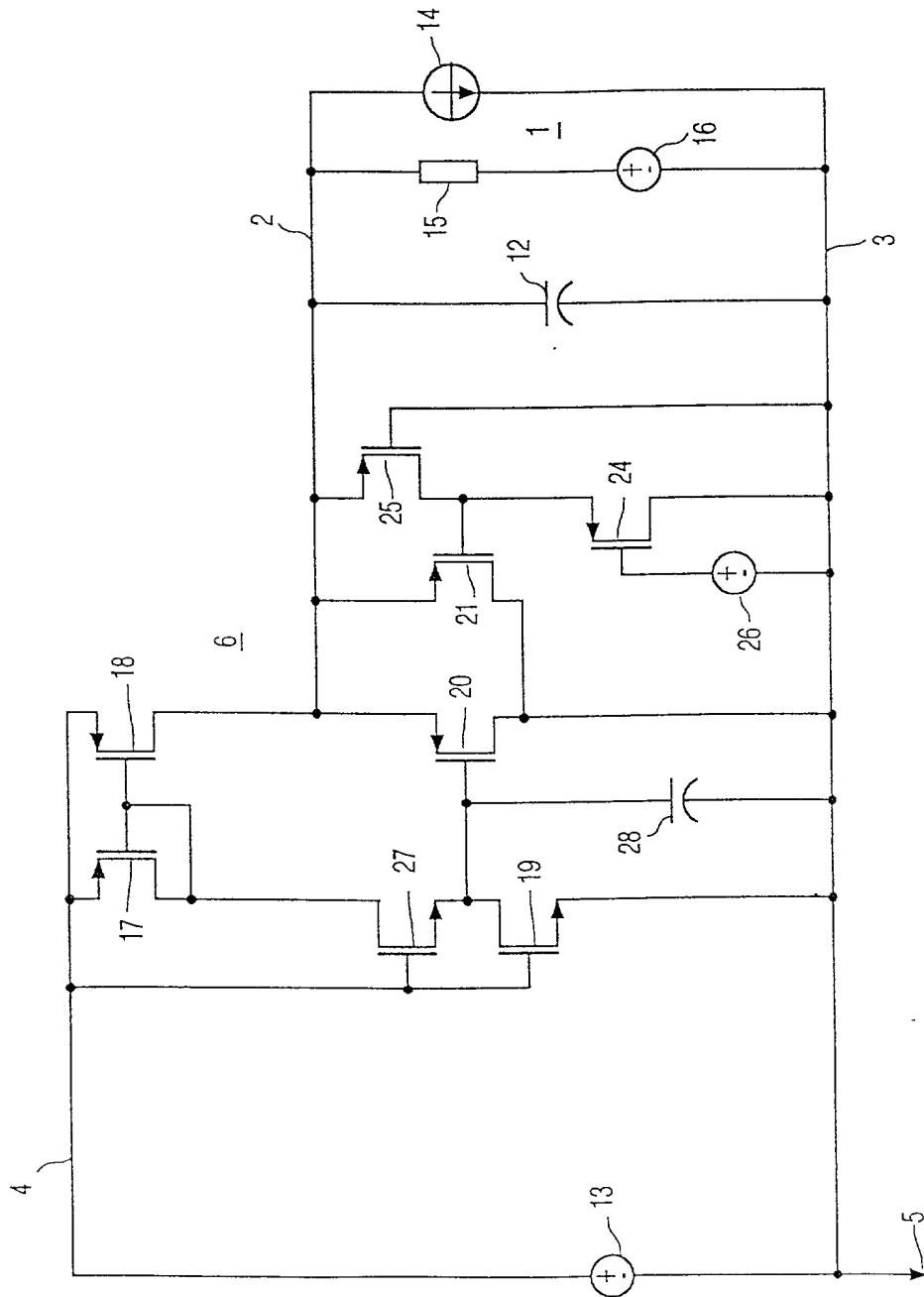


FIG. 4

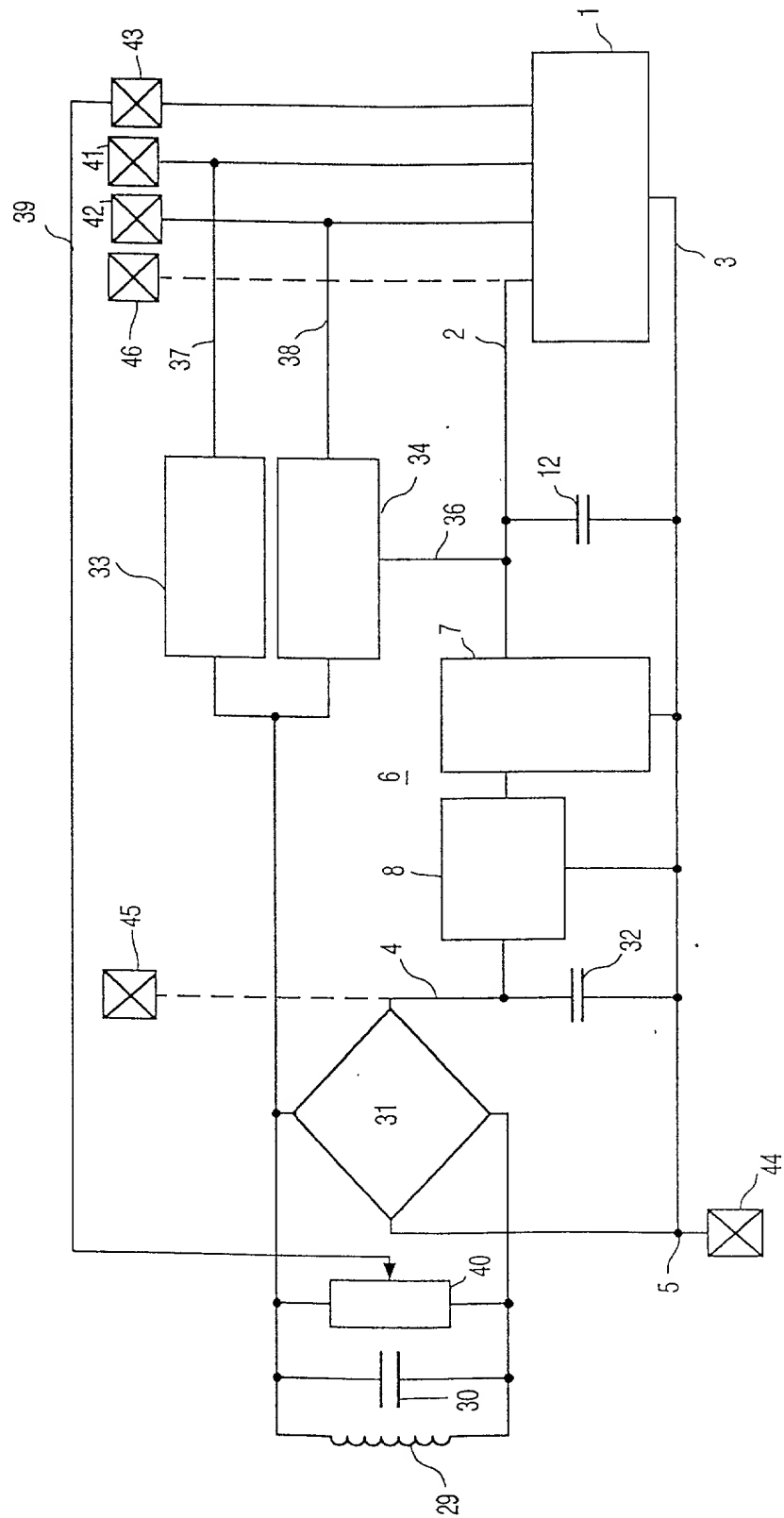


FIG. 5

As a below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated next to my name.

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled: "Mobile data carrier"

the specification of which (check only one item below):

☐ is attached hereto.

☐ was filed as United States application

Serial No _____

on _____

and was amended

on _____

☒ was filed as PCT international application

Number PCT/EP99/10405

on 24 December 1999 (24.12.99)

and was amended under PCT Article 19

on _____ (if applicable).

I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose information which is material to the examination of this application in accordance with Title 37, Code of Federal Regulations, § 1.56(a).

I hereby claim foreign priority benefits under Title 35, United States Code, § 119 of any foreign application(s) for patent or inventor's certificate or of any PCT international application(s) designating at least one country other than the United States of America listed below and have identified below any foreign application(s) for patent or inventor's certificate or any PCT international application(s) designating at least one country other than the United States of America filed by me on the same subject matter having a filing date before that of the application(s) of which priority is claimed:

PRIOR FOREIGN/PCT APPLICATION(S) AND ANY PRIORITY CLAIMS UNDER 35 U.S.C. 119:

COUNTRY	APPLICATION NUMBER	DATE OF FILING DAY, MONTH, YEAR	PRIORITY CLAIMED UNDER 35 USC 119
Europe	99100168.6	7 January 1999	YES
Germany	19900261.4	7 January 1999	YES




POWER OF ATTORNEY: As a named inventor, I hereby appoint the following attorney(s) and/or agent(s) to prosecute this application and transact all business in the Patent and Trademark Office connected therewith. (List name and registration number)

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205	FULL NAME OF INVENTOR	FAMILY NAME	FIRST GIVEN NAME	SECONDE GIVEN NAME
	RESIDENCE & CITIZENSHIP	CITY	STATE OR FOREIGN COUNTRY	COUNTRY OF CITIZENSHIP
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	RESIDENCE & CITIZENSHIP	CITY	STATE OR FOREIGN COUNTRY	COUNTRY OF CITIZENSHIP
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I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true: and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under section 1001 of Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

SIGNATURE OF INVENTOR 201 	SIGNATURE OF INVENTOR 202 	SIGNATURE OF INVENTOR 203 
DATE July 26, 2000	DATE July 26, 2000	DATE July 26, 2000
SIGNATURE OF INVENTOR 204	SIGNATURE OF INVENTOR 205	SIGNATURE OF INVENTOR 206
DATE	DATE	DATE